



DAC
set internal gain to 1
output range 0 to 2.047V

Level shift to bipolar
+/-1V

Sample and Hold

Difference Amplifier
Gain = 2

Slope adj

S&H / Ramp

Output Buffer
Gain = 2
To drive 50 ohm load

NOTE:
DG201 (or equivalent) must be a
modern variety that uses an
internally generated logic threshold
and does not require VL (pin 12).
Vishay-Silicon DG201BDJ is suitable.

Based on vector generator design by
Steve Ciarcia and Hal Chamberlin
"Make Your Next Peripheral a Real Eye Opener"
BYTE magazine, November 1976

Eye Opener 2K15	
WARNING: untested	
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Drawing Number:	
File: eyeopener2	REV:
Date: 7/14/15 11:07 PM	Sheet: 1/1